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## ABSTRACT

Efficient injection of charge from metal electrodes into semiconductors is of paramount importance to obtain high performance optoelectronic devices. The quality of the interface between the electrode and the semiconductor must, therefore, be carefully controlled. The case of organic semiconductors presents specific problems: ambient deposition techniques, such as solution processing, restrict the choice of electrodes to those not prone to oxidation, limiting potential applications. Additionally, damage to the semiconductor in sputter coating or high temperature thermal evaporation poses an obstacle to the use of many device-relevant metals as top electrodes in vertical metal–semiconductor–metal structures, making it preferable to use them as bottom electrodes. Here, we propose a possible solution to these problems by implementing graphene-passivated nickel as an air stable bottom electrode in vertical devices comprising organic semiconductors. We use these passivated layers as hole-injecting bottom electrodes, and we show that efficient charge injection can be achieved into standard organic semiconducting polymers, owing to an oxide free nickel/graphene/polymer interface. Crucially, we fabricate our electrodes with low roughness, which, in turn, allows us to produce large area devices (of the order of millimeter squares) without electrical shorts occurring. Our results make these graphene-passivated ferromagnetic electrodes a promising approach for large area organic optoelectronic and spintronic devices.

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Organic semiconductors serve as a platform for (opto)electronic devices with tunable characteristics by molecular design, enabling versatile device integration, and processing strategies.<sup>1</sup> However, ambient processing techniques such as solution processing can facilitate oxidation of metal contacts, resulting in an uncontrolled electronic interface, which is deleterious to performance in semiconductor devices.<sup>2,3</sup> New techniques are, therefore, required to control the interface between organic semiconductors and oxidizing metals while maintaining the possibility of solution processing.

Graphene has been shown to act as an atomically thin permeation barrier.<sup>4–6</sup> Graphene grown via chemical vapor deposition (CVD) directly on the surface of strongly interacting<sup>7</sup> catalytic metals,

such as Ni, Co, or Fe, acts as a barrier layer to prevent oxidation.<sup>8–11</sup> These oxide-free ferromagnetic interfaces have been shown to hold significant benefits within the field of spintronics,<sup>12,13</sup> as they enable oxidative fabrication processes, such as solution processing<sup>9,10</sup> or atomic layer deposition,<sup>14</sup> to be used to fabricate devices with a wider range of relevant materials. One appealing possibility would be to develop graphene-passivated ferromagnets as electrodes<sup>9,10</sup> for organic semiconductor spintronics,<sup>15–18</sup> where the quality of the electronic interface between the ferromagnetic electrode and the organic semiconductor is of paramount importance.<sup>19,20</sup> Another important advantage of an ambient-stable ferromagnetic layer is that it can be used as a bottom electrode in vertical metal-organic semiconductor–metal

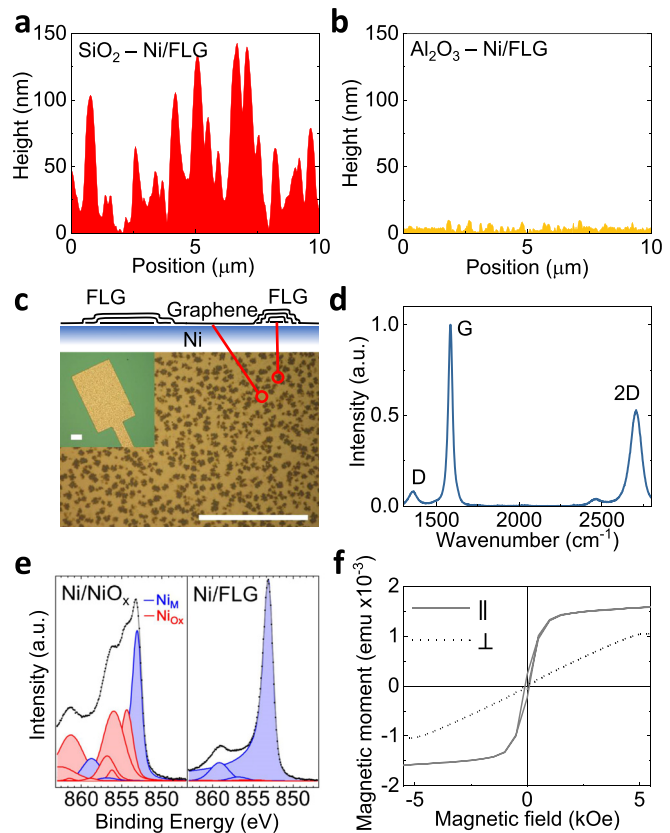
structures, allowing one to employ techniques such as sputtering to obtain high quality and thickness-controlled metal layers or multilayers; note that sputtering cannot be used for top-electrodes as it would destroy<sup>21</sup> the organic semiconductor.

Previous reports using graphene passivated ferromagnets as electrodes for organic semiconductor devices have studied the spin injection properties<sup>10</sup> as well as charge injection in lateral organic semiconductor field effect transistors.<sup>9</sup> In this work, we investigate few-layer graphene-passivated nickel (Ni/FLG) as a bottom electrode for injection of holes into organic semiconducting polymers in a vertical device structure, demonstrating efficient injection into two standard semiconducting polymers deposited from solution and in air, directly on top of Ni/FLG. Compared to previous reports on graphene-passivated ferromagnetic electrodes, where lithographic techniques had to be used in order to produce micrometer-sized features,<sup>8–10,12,14</sup> here, we were able to produce working devices with several orders of magnitude larger active area (4.5 mm<sup>2</sup>). Our results are, thus, encouraging for the further development of organic optoelectronic and spintronic devices processed from solution under ambient conditions.

Nickel was initially sputtered on thermally oxidized silicon wafers, producing films with a thickness of 150 nm. FLG domains were grown on such sputtered Ni films in a custom low-pressure Chemical Vapor Deposition (CVD) reactor (base pressure  $\sim 1 \times 10^{-6}$  mbar). All substrates were cleaned by sonicating in acetone followed by isopropyl alcohol and blow-dried with a nitrogen gun before loading. Samples were heated to approximately 450 °C using a resistive heater (temperature measurements by a K-type thermocouple) with a rapid ramp rate of 100 °C/min and annealed at  $\sim 1$  mbar of H<sub>2</sub> for 10 min. This reduces the native oxide prior to graphene growth. After annealing, the H<sub>2</sub> flow was stopped and the chamber was evacuated back to approximately base pressure over a period of 5 min. For graphene growth, C<sub>2</sub>H<sub>2</sub> gas was gradually introduced into the reactor via a mass flow controller by incrementally increasing the flow rate over 5 min to achieve a partial pressure of C<sub>2</sub>H<sub>2</sub> of  $2.5 \times 10^{-4}$  mbar. Subsequently, the samples were held at 450 °C in  $2.5 \times 10^{-4}$  mbar of C<sub>2</sub>H<sub>2</sub> for a further 25 min, before rapid cooling (initially  $\sim 300$  °C/min) while maintaining the C<sub>2</sub>H<sub>2</sub> flow. All gases were stopped once room temperature had been reached.

Upon graphene growth, a roughening of the Ni sputtered on thermally oxidized Si was observed, with an RMS = 67 nm [Fig. 1(a)]. The roughness was found to increase with increasing growth temperature. The roughening of Ni upon graphene growth is explained by grain growth in the sputtered Ni films, occurring at high temperatures during the CVD process: under these conditions, the internal forces in the film are larger than those between the film and the substrate, and diffusion of the film material is appreciable.

Surface roughness is a significant problem in our desired vertical metal-semiconductor-metal devices: to obtain the cleanest interface and achieve efficient charge injection, the organic semiconductors are spin coated from solution directly on top of the Ni-graphene film without the use of roughness-reducing intermediate layers; however, if the surface of the Ni-graphene film is too rough, Ni protrudes through the film and can directly come into contact with the top electrode, causing electrical shorting of the devices. While a thick layer of organic semiconductor may be used to contain this problem, the operating voltage of the devices will scale with the semiconductor thickness, resulting in impractical voltages being required to operate the devices.



**FIG. 1.** AFM line scan of sputtered Ni films after graphene growth using a Si-SiO<sub>2</sub> substrate without (a) and with (b) an Al<sub>2</sub>O<sub>3</sub> adhesion layer. (c) Optical micrograph of a graphene-passivated Ni electrode, including a cartoon indicating areas with different numbers of graphene layers. Scale bars are 50 μm. (d) Raman spectrum of few-layer graphene on Ni. (e) XPS of the Ni 2p<sub>3/2</sub> region of sputtered Ni (Ni/NiO<sub>x</sub>—black) and Ni after multilayer graphene growth (Ni/FLG—green), with peak fitting indicating metallic nickel (Ni<sub>M</sub>) and nickel oxide (Ni<sub>Ox</sub>). (f) Magnetic moment measured at room temperature by DC-mode SQUID magnetometry of graphene-passivated Ni films for magnetic fields parallel (gray, solid) and perpendicular (black, dots) to the plane of the film.

To solve the issue of roughness, we reduced grain growth by adding a 10 nm Al<sub>2</sub>O<sub>3</sub> adhesion-promoting layer,<sup>22</sup> grown by atomic layer deposition on the Si wafer prior to Ni deposition. The Al<sub>2</sub>O<sub>3</sub> adhesion-promoting layer sensibly reduced the surface roughness [Fig. 1(b), RMS = 4.3 nm]: as presented below, this allowed us to reliably fabricate working devices showing no electrical shorts and with a surface area of 4.5 mm<sup>2</sup>, which is large compared to commonly studied vertical spintronic devices (typically on a scale between 1 and 10<sup>4</sup> μm<sup>2</sup>).<sup>8–10,12,14</sup>

Optical microscopy [Fig. 1(c)] and Raman spectroscopy [Fig. 1(d)] confirmed that the electrodes were fully covered with graphene, notably with islands of multiple layers, as is often observed under similar growth conditions on Ni thin films.<sup>11,23,24</sup> Such thicker multilayer islands appeared as dark sections in optical microscopy, as shown schematically in Fig. 1(c). Raman spectroscopy was carried out using a Renishaw in Via confocal Raman microscope and a laser excitation wavelength of 532 nm. We observed a D peak [Fig. 1(d)], which we

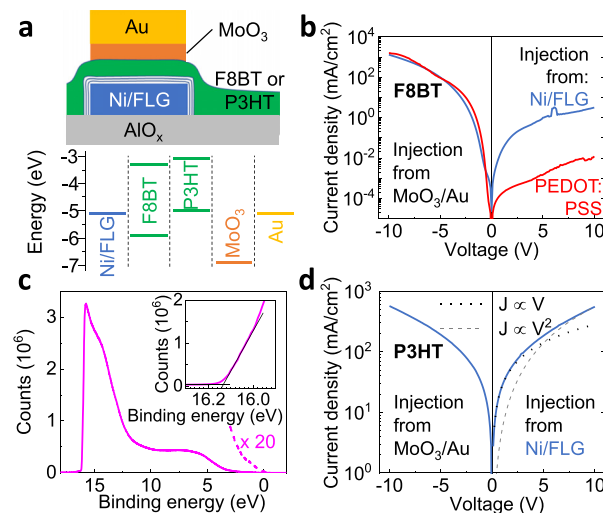
ascribe to the relatively low temperature of the growth process. The broad 2D peak ( $\text{FWHM} = 79 \text{ cm}^{-1}$ ) and  $G/2D$  ratio  $>1$  are also consistent with multilayer growth.<sup>25</sup>

X-ray photoelectron spectroscopy (XPS) was carried out using a Thermo Scientific ESCALAB 250Xi with a micro-focused and monochromated Al  $K\alpha$  x-ray source. All spectra were background-corrected (Shirley) and analyzed by performing a nonlinear mean square fit of the data, using Doniach–Šunjić functions convoluted with Gaussian profiles. Measurements of the  $\text{Ni}2p_{3/2}$  region confirmed that the sputtered Ni is initially oxidized during air transfer, with significant nickel oxide/hydroxide ( $\text{NiO}_x$ ) components visible [Fig. 1(e), left]. Following graphene growth, the previously oxidized Ni is reduced to metallic Ni and remains reduced despite transfer of the electrodes through air, as confirmed by the dominant  $\text{Ni}_M$  components and negligible contributions from  $\text{NiO}_x$  species [Fig. 1(e), right]. This effective passivation is attributable to the strong interaction between graphene and Ni and the fact that even if defects are present in the graphene, a passivating oxide is formed locally, which does not propagate laterally.<sup>26</sup> Herein, we used an extended hydrocarbon exposure with the intention of blocking as many defects as possible through the formation of thicker FLG at these locations, where hydrocarbon can continue to access the Ni surface [Fig. 1(c)].

We used a Quantum Design SQUID magnetometer (MPMS3 model) to determine the room temperature magnetic properties of the Ni films after graphene growth [Fig. 1(f)]. With a standard DC-mode measurement, we determined the magnetization of our films parallel and perpendicular to the magnetic field: consistent with previous observations,<sup>27</sup> the easy-axis of magnetization of the Ni films is parallel to the plane of the film. The films also show the typical small hysteresis of nickel, that is of a “soft” ferromagnetic metal. These measurements, thus, confirm that the bulk ferromagnetic properties of the Ni layers are maintained after graphene passivation.

We then fabricated devices with the structure schematically depicted in Fig. 2(a). Electrodes consisting of 150 nm thick stripes of Ni were patterned by using shadow masks on a Si wafer with an  $\text{Al}_2\text{O}_3$  adhesion layer; few-layer graphene was grown on top of Ni as described above. We first studied hole injection into the organic semiconducting polymer F8BT (poly(9,9-dioctylfluorene-alt-benzothiadiazole)), a polymer commonly used in green emitting organic light emitting diodes (OLEDs). F8BT was previously received from Cambridge Display Technology, and it was spin coated in air directly on top of the bottom electrode. A solution of 15 mg/ml in para-xylene was used for spin-coating, giving smooth and homogeneous films with a thickness of 180 nm (measured using a Dektak profilometer). A top electrode consisting of  $\text{MoO}_3/\text{Au}$  (8 nm/100 nm) was then deposited by thermal evaporation at a pressure of  $10^{-7}$  mbar.

The device area, determined by the overlap of the top and bottom contacts, was  $4.5 \text{ mm}^2$ . The  $\text{MoO}_3/\text{Au}$  contact was used to study Ohmic injection into the deep-lying HOMO level of F8BT ( $-5.9 \text{ eV}$ ).<sup>28</sup> Ohmic injection of holes has been demonstrated previously using  $\text{MoO}_3$  and is due to surface p-doping of the polymer by  $\text{MoO}_3$  at the polymer interface,<sup>29</sup> which results in barrier-free injection.<sup>30</sup> Figure 2(b) shows the current density–voltage characteristic measured on the devices. Negative voltages correspond to injection of holes from  $\text{MoO}_3/\text{Au}$ : Ohmic injection is achieved as expected, evidenced by high currents reaching the space-charge limit at high absolute voltages.



**FIG. 2.** (a) Schematic representation of the device structure used. (b) Comparison of hole injection into F8BT from graphene-passivated Ni (blue) and PEDOT:PSS (red) bottom contacts, each using a top contact of  $\text{MoO}_3/\text{Au}$ . (c) Binding energy, referenced to the Fermi level, measured on a Ni/FLG sample by means of UPS (excitation light with energy  $h\nu = 21.21 \text{ eV}$ ). The inset shows the high binding energy part of the spectrum used to extract the cutoff energy  $E_{\text{CO}}$ . The work function  $\phi$  is calculated as  $\phi = h\nu - E_{\text{CO}}$ . For better visualization, the part of the curve at the Fermi level is also shown after 20-fold magnification (dashed line). (d) Hole injection into P3HT from graphene-passivated Ni, with the  $\text{MoO}_3/\text{Au}$  top contact.

The Ni/FLG devices were compared against reference devices that have the same surface area and consist of the standard structure ITO/PEDOT:PSS/F8BT/ $\text{MoO}_3/\text{Au}$ . For PEDOT:PSS, the Heraeus Clevis™ AI 4083 formulation was used. The PEDOT:PSS film was deposited in air on top of a cleaned, pre-patterned ITO substrate and subsequently annealed under nitrogen for 20 min at  $150^\circ\text{C}$ . The remaining part of the vertical stack was fabricated as in the case of Ni/FLG-based devices.

When injecting from  $\text{MoO}_3/\text{Au}$ , the same behavior as in the Ni/FLG devices was observed. When injecting from PEDOT:PSS into F8BT, current densities ca. five orders of magnitude lower were observed, consistent with injection of holes being barrier-limited. While we still observed a barrier-limited injection, when injecting holes from Ni/FLG, we recorded current densities that were 2–3 orders of magnitude higher than that in the case of PEDOT:PSS.

We further characterized the Ni/FLG layers to obtain more information about their electrical properties in devices. We measured the resistivity of the Ni/FLG films using the four-point probe method and recorded a resistivity of  $1.5 \times 10^{-7} \Omega \text{ m}$ . This translates in a series resistance of the whole Ni/FLG lead of  $\sim 1 \Omega$ , therefore representing a negligible contribution to the total resistance of the device.

We then measured the work function of our Ni/FLG substrates by means of ultraviolet photoelectron spectroscopy (UPS) using a Thermo Fisher Scientific ESCALAB 250Xi x-ray/Ultraviolet (XPS/UPS) photoelectron spectrometer, with an excitation wavelength of 21.21 eV; consistent with previous reports, our Ni/FLG samples show a work function of 5.07 eV [Fig. 2(c)], which is close to the work function of nickel.<sup>31–33</sup> A substantial energy barrier is, therefore, present at the Ni/FLG/F8BT interface, resulting in the barrier-limited injection



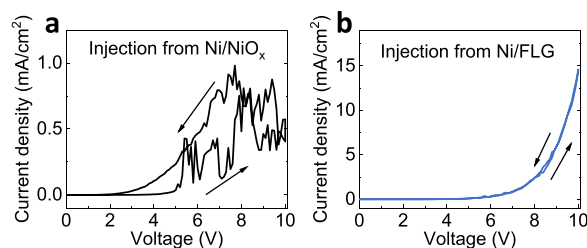
observed in Fig. 2(b). Note that the measured work function of Ni/FLG is comparable to the work function of the same PEDOT:PSS formulation, previously reported to be 5.1 eV.<sup>34</sup> However, charge accumulation at the Ni/FLG/F8BT interface under voltage bias could shift the work function downwards, as it has been previously shown in graphene;<sup>35,36</sup> we hypothesize that this effect might contribute to a lower effective injection barrier at the interface with Ni/FLG compared to PEDOT:PSS, thus explaining the difference in current density observed at positive bias in F8BT-based devices [Fig. 2(b)].

We tested the same Ni/FLG/polymer/MoO<sub>3</sub>/Au structure using another standard semiconducting polymer: P3HT (Poly(3-hexylthiophene-2,5-diyl)). P3HT was purchased from Rieke Metals in its regular version (catalogue number RMI-00EE) and dissolved in a chlorobenzene:chloroform mixture (50:50, volume:volume) using a concentration of 30 mg/ml; spin-coating in air yielded smooth and homogeneous films of 200 nm thickness (measured using a Dektak profilometer). A MoO<sub>3</sub>/Au (8/100 nm) top electrode was evaporated on top of the polymer layer, as in the case of the previously discussed F8BT devices.

In this case, we observed high currents both when injecting from MoO<sub>3</sub>/Au and when injecting from Ni/FLG [Fig. 2(d)], with a very symmetric current density–voltage curve, linear at low voltages and reaching the space-charge current limit at higher voltages (as testified by a  $V^2$  dependence of the current density). The perfectly symmetric J–V plot demonstrates that hole injection in P3HT is barrier-free from both contacts. This result is consistent with the absence of an energy barrier between the HOMO level of P3HT (5.0 eV) and Ni/FLG (5.07 eV as measured in UPS).

To further investigate the electronic quality of nickel passivated electrodes, we compared the hole injection into F8BT from passivated nickel with the case of non-protected nickel electrodes (Fig. 3). Here, we tested smaller scale devices with an active area of 0.05 mm<sup>2</sup>, fabricated by employing a resist mask patterned by photolithography for the deposition of Ni followed by lift-off in acetone. The applied voltage was set up to probe hole injection from Ni/NiO<sub>x</sub> into F8BT. We observed that hole injection from unpassivated Ni/NiO<sub>x</sub> has a very low efficiency [Fig. 3(a)], indicated by low and unstable current densities. A significant improvement is observed with graphene passivation [Fig. 3(b)], suggesting that the native oxide of Ni has a detrimental effect on charge injection.

Furthermore, in Fig. 3, the voltage is swept first from 0 V to 10 V and then 10 V to 0 V at a sweep rate of 2 V s<sup>−1</sup> so that any hysteretic effects can be studied. Our Ni/NiO<sub>x</sub> devices show not only strong instabilities in current but also large hysteretic behavior. We explain



**FIG. 3.** Current density hysteresis as a function of voltage for hole injection into F8BT using (a) unprotected sputtered Ni electrodes and (b) graphene-passivated Ni. The arrows indicate the direction of voltage sweep.

the observed unstable and hysteretic currents as originating from resistive switching processes taking place in NiO<sub>x</sub>. Resistive switching is well known to occur in NiO<sub>x</sub>, supported by the presence of defects (e.g., oxygen vacancies).<sup>37</sup> The behavior displayed by our Ni/NiO<sub>x</sub>/F8BT devices suggests that conductive filaments of metallic nickel are formed in the NiO<sub>x</sub> native layer upon the application of the voltage bias, causing the spikes in current. A fraction of these conductive filaments is likely to persist during the whole sweep and to give rise to the hysteresis; note that the NiO<sub>x</sub> layer formed on our electrodes during air exposure is expected to be thin (a few nanometers),<sup>38</sup> resulting in large electric fields across it at the voltages used in this study. Supporting this interpretation, graphene-passivated electrodes do not show any significant hysteresis, while presenting a much higher density of injected current [Fig. 3(b)], consistent with the current densities in the large area device [Fig. 2(b)].

In conclusion, we were able to fabricate clean interfaces based on nickel passivated with few-layer graphene, providing efficient charge injection into solution-processed organic semiconductor layers. Crucially, we were able to fabricate such layers as bottom electrodes with very low roughness, which enabled their employment in large area vertical metal–semiconductor–metal devices, up to two orders of magnitude larger than that of the previously reported organic devices, based on graphene-passivated cobalt.<sup>10</sup> Our results provide a route to retain two main advantages offered by organic semiconductors, large area and solution processing in air, while also producing high quality ferromagnetic electrodes with clean interfaces, the latter being a necessary requisite for achieving high performance optoelectronic and, particularly, spintronic devices.

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Data sets related to this publication are available from the Cambridge University data repository at <https://doi.org/10.17863/CAM.51196>.

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