Resilience Pathways for Halide Perovskite Photovoltaics Under Temperature Cycling

Luyan Wu (吴录艳)[1,2]†, Shuaifeng Hu (胡帅锋)[3]†, Feng Yang (杨枫)[4,5]†, Guixiang Li (李桂香)[1,6]*, Junke Wang (王竣可)[3], Weiwei Zuo (左巍巍)[7], José J. Jerónimo-Rendon[7], Silver-Hamill Turren-Cruz[8], Michele Saba[2], Michael Saliba[7,9], Mohammad Khaja Nazeeruddin[6], Jorge Pascual[10]*, Meng Li (李萌)[4]*, Antonio Abate[1,11]*

[1]Helmholtz-Zentrum Berlin für Materialien und Energie GmbH, Hahn-Meitner-Platz 1, 14109 Berlin, Germany.

[2]Department of Physics, Università degli Studi di Cagliari, Monserrato (CA), I-09042, Italy.

[3]Clarendon Laboratory, Department of Physics, University of Oxford, Oxford OX1 3PU, U.K.

[4]Key Lab for Special Functional Materials of Ministry of Education, National & Local Joint Engineering Research Center for High-efficiency Display and Lighting Technology, School of Nanoscience and Materials Engineering, and Collaborative Innovation Center of Nano Functional Materials and Applications, Henan University, Kaifeng 475004, P. R. China.

[5]College of Physics, Henan Key Laboratory of Advanced Semiconductor & Functional Device Integration, Henan Normal University, Xinxiang 453007, China.

[6]Institute of Chemical Sciences and Engineering, École Polytechnique Fédérale de Lausanne (EPFL), 1015 Lausanne, Switzerland.

[7]Institute for Photovoltaics (ipv), University of Stuttgart, Pfaffenwaldring 47, D-70569 Stuttgart, Germany.

[8]Instituto de Ciencia de los Materiales (ICMUV), Universidad de Valencia, C/Catedrático José Beltrán 2, E-46980 Paterna, Spain.

[9]Helmholtz Young Investigator Group FRONTRUNNER, IEK5-Photovoltaics Forschungszentrum Jülich 52425 Jülich, Germany.

[10]Polymat, University of the Basque Country UPV/EHU, 20018 Donostia-San Sebastian, Spain.

[11]Department of Chemistry, Bielefeld University, Universitätsstraße 25, 33615 Bielefeld, Germany

† These authors contributed equally: L. Wu, S. Hu and F. Yang

* Corresponding authors. E-mail: guixiang.li.gl@gmail.com (G. Li), jpascual1992@gmail.com (J. Pascual), mengli@henu.edu.cn (M. Li), antonio.abate@helmholtz-berlin.de (A. Abate)

Abstract

Metal-halide perovskite solar cells have achieved power conversion efficiencies comparable to those of silicon photovoltaic devices, approaching 27% for single-junction devices. The durability of the devices, however, lags far behind their performance. Their practical implementation implies the subjection of the material and devices to temperature cycles of varying intensity due to diurnal cycles or geographical characteristics. Thus, it is vital to develop devices that are resilient to temperature cycling. This Perspective analyses the behavior of perovskite devices under temperature cycling. We discuss the crystallographic structural evolution of the perovskite layer, reactions and/or interactions between stacked layers, photovoltaic properties, and photocatalysed thermal reactions. We highlight effective strategies for improving stability under temperature cycling, such as improving material crystallinity or relieving interlayer thermal stress using buffer layers. Additionally, we outline existing standards and protocols for temperature cycling testing and we propose a unified approach that could facilitate valuable cross-study comparisons among scientific and industrial research labs. Finally, we share our outlook on strategies to develop perovskite photovoltaic devices with exceptional real-world operating stability.

ToC blurb: Device resilience under temperature cycling is critical for the practical implementation of perovskite photovoltaics. This perspective highlights how the stability evolves with thermal cycling, emphasizing the need for adequate protocols and a deeper understands of the materials and interfaces changes.

[H1] Introduction

Since their early introduction, metal-halide perovskite solar cells (PSCs) have rapidly developed owing to their exceptional photovoltaic (PV) properties[1-6] and low-cost solution- and vacuum-based fabrication[7-10]. The leading power conversion efficiencies (PCE) of single-junction PSCs now approach 27%[11,12] and is on par with that of silicon cells. In tandem PVs, double-junction devices with two stacked photo absorbers in series could achieve a potential PCE exceeding 45%[13], benefiting from the reduced thermalisation loss of the excitons. Thanks to the bandgap tunability[14] and ultra-long carrier diffusion length[15] of metal halide perovskites, depositing, for instance, a 1 µm thick layer of perovskite with a bandgap of ~1.7 eV atop a conventional silicon subcell (~1.1 eV) enables PCE of over 34% in lab scale small cells and of over 30% in commercial wafer-scale devices. Moreover, monolithic double-junction "all-perovskite" tandems have achieved PCE values of over 30%[16]. In principle, multi-junction perovskite-based tandems could reach PCE values of over 50%, constituting the most cost-effective PV products on the market[13]. Efficiency is thus the main asset of PSCs, taking these semiconductors a step closer to practical implementations.

The stability of perovskite-based materials and devices, mostly regarding defect generation, ion migration, phase transition and interface degradation, brings up important concerns regarding their future practical potential. To date, studies on perovskite device stability have primarily focused on operational stability at room temperature[17,18] or on accelerated ageing[19,20] (for example, under elevated fixed temperature such as +85 °C) to extrapolate the "potential long-term lifespan"[21-31]. However, during practical operation, PV devices are constantly exposed to temperature changes. Thus, to accurately predict the stability of perovskite PV devices under practical operation[32], they need to be studied under temperature cycling, which is a condition that is scarcely explored. Variations in temperature have a critical influence on the structural and chemical changes of the materials, the device structure, and the PV performance, which is different from the exposition to fixed temperatures. Thus, understanding the impact of temperature and temperature cycles on materials and devices, and performing the corresponding stability analysis, is of utmost importance for developing long-term stable perovskite PV technology.

The temperature fluctuates with diurnal cycles, seasonal variations, altitude differences, and diverse geographical locations (from the poles to the equator)[33]. For instance, extreme temperature changes, for example in deserts, pose additional challenges for the implementation of perovskite PVs[34-38]. Other applications, such as extra-terrestrial use (for example in spacecraft, high-altitude pseudo-satellites, international space stations, and Mars rovers)[36,39-42], impose even more severe conditions due to a larger temperature range[32,43,44]. These temperature cycles have a substantial impact on the lifespan of PV devices[45-47], influencing properties such as lattice strain, crystallographic site defects, phase

transitions, ion migration, and material diffusion between and among the stacked device layers[48-52]. Together with the influence of photons and other stressors, temperature cycles could lead to accelerated device failure compared to the commonly employed elevated constant temperature[19,53-55], and are more representative of the practical outdoor behavior of the devices[56]. Thus, summarizing and understanding the working and failure mechanisms of perovskites and their devices under thermal cycling is particularly meaningful for the development of commercial perovskite-containing PVs.

In this Perspective, we discuss the importance of temperature cycling when evaluating long-term perovskite PV stability. We aim to emphasise the temperature-variation-induced degradation of PSCs, which is often disregarded in comparison to degradation at elevated temperatures. We present a systematic overview of the structure and performance evolution of perovskite-based PV devices under temperature cycles, and we compare perovskite materials of different bandgaps to highlight the influence of perovskite composition on temperature-variation-induced degradation. We also propose strategies to improve operational stability under temperature cycling, and we recommend suitable protocols for assessing device evolution under extended thermal cycling. Lastly, we share our opinions on the future challenges for developing perovskite PV devices resistant to temperature cycles.

[H1] Impacts of temperature cycling on perovskite photovoltaic devices

Temperature cycling has different impacts on perovskite materials and PV devices. In the perovskite device stack, the layers most affected by temperature cycling are the perovskite layer and the charge transporting layers (CTLs). In the perovskite layer, phase transitions are temperature-dependent and are strongly influenced by perovskite composition. Temperature cycling might lead to irreversible phase transitions and lattice distortions due to strain (**Fig. 1**). In addition, temperature cycling promotes the interaction, diffusion and penetration of perovskite components with the CTLs and its interfaces. Furthermore, dynamic fluctuations in CTLs due to temperature can exert additional strain on the perovskite layer. Finally, temperature has a direct effect on PV parameters. Although the influence of temperature on PV parameters is usually reversible, consecutive cycling can induce a permanent declining trend in the performance.

[H2] Impact on the perovskite layer

Temperature cycling has two main consequences in the integrity of the perovskite layer. First, perovskite crystalline phase is dependent on the environment temperature, therefore consecutive cycles of temperature will impose successive phase transitions that can lead to irreversible degradation. Second, temperature-driven lattice distortions introduce lattice strain in the films that will accumulate with increased cycling.

[H3] Phase transitions

Temperature drives perovskite phase changes by affecting thermal energy, atomic vibration, and ionic migration (**Fig. 2a**). In general, under thermal cycling, two distinct phase transition behaviors impact the performance of the device.[57] The first is a reversible phase transition between the cubic and tetragonal phases, which accounts for properties variation within a single thermal cycle. The second is an irreversible phase transition, such as the transformation to the hexagonal photoinactive polytype phase (that is, 4H and 6H)[58] and decomposition to PbI_2, which results in continued and unrecoverable degradation of the perovskite and device (**Fig. 2b**). The tetragonal phase (β-phase) is usually retained in the low-temperature region after multiple thermal cycles[57,59]. Phase transitions are more readily reversible in the out-of-plane direction compared to the in-plane direction where additional forces are exerted by substrate. Under such reversible behavior, device performance is unlikely to experience significant degradation when the temperature returns to normal operating conditions.

In addition, the evolution of the perovskite structure when subjected to temperature cycles strongly depends on the composition of the material. In the archetypical perovskite, $MAPbI_3$, for example, the cubic phase evolves to the tetragonal phase at temperatures below 327 K, with a further transition to the orthorhombic phase below 162 K[60]. Meanwhile, when the temperature reaches over 350 K, the thermally-induced release of organic cations (MA^+) and vaporisation of the amine (MA) leads to the degradation of the perovskite crystal[61,62]. In FAMACs-based perovskite (FA = formamidinium)[63,64], the transition from the original cubic phase to the tetragonal phase occurs at ~240 K, whereas the transition from the tetragonal to the orthorhombic phase does not occur above 100 K. The different phase transition temperatures between these two perovskite compositions suggest that perovskite composition is pivotal in determining its phase transition dynamics. In theory, maintaining the corner-sharing assembly of the perovskite octahedral units in the 3D structure is critical to preserving its optoelectronic properties. This understanding helps deepen insights into the perovskite structure, directly impacting the device's reliability.

[H3] Strain

Beyond phase transitions, continuous lattice distortions can occur throughout the entire temperature cycling process, and the lattice strain plays a central role[65,66]. In solar cells, especially tandem cells, the mismatch between the coefficients of thermal expansion (CTEs) of the absorber(s) and the functioning layers will accordingly lead to a considerable non-uniformity in the strain distribution, especially at the interfaces, under temperature cycling. In some severe cases, fast periodical temperature changes can accelerate fatigue or even device failure due to the mechanical delamination of the stack(s). In addition, each layer can also show variations in strain distribution, especially as a function of the film thickness. The impact from the strain also varies by direction; for example, strain along the out-of-plane direction is stronger and more releasable than in the in-plane direction due to

the presence of adjacent layers along the in-plane direction. This additional strain is concentrated on the defective areas of the interface, leading to delamination after temperature cycling[32]. Compared to fixed temperatures, thermal cycling introduces repetitive mechanical stress and generates dynamic strain due to the constant expansion and contraction of layers with mismatched CTEs. Therefore, these mechanical and interfacial challenges need to be addressed to improve the durability and reliability of devices under dynamic operating conditions. The introduction of a heterochiral interface has been validated to improve adhesion and mitigate fatigue under repeated thermal cycling (−40 to +85 °C for 200 cycles), to address mechanical failures induced by mismatches in the CTEs[67]. Future research can build on such approaches to further enhance the long-term stability of perovskite-based devices.

[H2] Impact on charge transport layers

Charge transport materials' (CTMs) characteristics have a substantial influence on the stability of PSCs during temperature cycling, which brings into question whether the thermal stability of the devices is due to the inherent thermal stability of the CTMs themselves. Indeed, CTMs that possess robust thermal properties are associated with limited interfacial defect generation and have a consistent energy level alignment with the perovskite layer across applied temperature ranges. Employing poly[bis(4-phenyl)(2,4,6-trimethylphenyl)amine] (PTAA) instead of spiro-OMeTAD as the CTM, for example, leads to sustained operational efficiency even at elevated temperatures because of the its low glass transition temperature[68,69,70]. Nevertheless, temperature cycles have additional implications on the material and device evolution, which are not only related to their intrinsic thermal stability[71].

Temperature cycling, in addition to a fixed high temperature, expedites interlayer material diffusion due to the dynamic character of the system under consecutive temperature fluctuations. In particular, ion migration is responsible for device instability due to degradation processes caused by ion rearrangement in the perovskite films and/or redox chemical reactions between migrated ions, especially halides, and active species in the devices[72-75]. In addition, the migration of ions departing from perovskite films, that is, precursor cations and anions, can accelerate the degradation of the transporting materials. For example, iodide from the perovskite layer can diffuse across the interface into the adjacent layers. Similarly, metals in the top electrode, such as Ag, Au or Cu, can also diffuse across the buffer and transport layers into the perovskite films[76]. As material diffusion is largely accelerated during temperature cycles, materials with conductivity values that show low dependence on temperature should be chosen to mitigate performance decay.

CTLs should also have CTEs that are aligned with those of the perovskite layer. Within PSCs, the attributes of individual materials in the different layers undergo dynamic fluctuations in response to temperature cycles (**Fig. 2c**). Disparate CTEs between CTMs and perovskite lead to periodic

contractions and expansions at varying rates. When the CTL has a lower CTE than the perovskite (~3.3×10^{-5} to 8.4×10^{-5} K^{-1}), the CTL constrains the perovskite along the in-plane directions during cooling, resulting in in-plane tensile strain, especially a few nanometre deep in the perovskite layer. Meanwhile, if the CTL has a higher CTE than the perovskite, the opposite may occur. Such interlayer stress could potentially lead to the deformation of chemical bonds and to defect formation at the interfaces, increasing the accumulation and recombination of charge carriers at defect sites and subsequently decreasing the cell performance. In more severe instances, this mismatch may cause delamination and/or fissure propagation, thereby substantially compromising both the efficiency and durability of the devices[77,78]. If the CTL is thin enough, the strain within the perovskite layer might be negligible compared to the strain exerted by the thicker and stiffer substrate. In this case, the substrate is dominant in controlling the strain in the perovskite. The stress caused by the mismatch in CTE may be balanced by reducing or counteracting the forces between the compressive and tensile layers[79]. Using polymer substrates with CTEs closer to that of perovskite layers has been confirmed to well mitigate stress caused by CTE mismatch[79].

[H2] Device performance following temperature cycling

Temperature cycles ultimately lead to changes in the PV performance of devices, which can be reflected in the variations of the different device parameters: PCE, short-circuit current density (J_{SC}), open-circuit voltage (V_{OC}), fill factor (FF), and hysteresis index (HI). The PCE values typically show small variations under temperatures ranging from room temperature to +150 °C (**Fig. 3a**). As the temperature decreases (from room temperature to −160 °C), however, the PCE begins to decrease, showing an evolution pattern close to that of FF values, whereas the V_{OC} slightly increases. This similarity suggests a tight relationship between the PCE and FF upon the decrease of the temperature, which is likely associated with temperature-related charge extraction events. Unlike conventional solar cells, the increased V_{OC} is not solely attributable to an enlarged bandgap (E_g) at low temperatures, as the E_g of the perovskite decreases slightly with temperature cooling down[64]. It is rather linked to the suppression of trap-assisted non-radiative recombination in both the surface and bulk of the perovskite layer. Meanwhile, the HI considerably increases as the temperature decreases, which is evidenced by comparing the reverse and forward scans of *J-V* curves (**Fig. 3a**).

From the viewpoint of thermodynamic, the phase transition from the cubic to the tetragonal phase with lowered temperature enables the effective elimination of intrinsic point defects in perovskite films, extending the carrier lifetime and enhancing the V_{OC} in devices (**Fig. 3b-c**)[64]. Intriguingly, device performance is recoverable after a limited amount of temperature cycles, and the PCE can even improve after one low-temperature cycle[64]. J_{SC}, V_{OC}, and FF remain stable when returned to room temperature after undergoing thermal cycles (**Fig. 3d**)[32]. This resilience may be linked to the

restoration of the perovskite structure following temperature fluctuations, which suggests that PSCs have great potential in variable temperature environments, especially those that fluctuate around room temperature. However, as the number of temperature cycles increases, the PCE starts to decline owing to irreversible alterations in the device structure. These alterations include the accumulation of PbI_2, the formation of the hexagonal phase in the perovskite layer, unreleased lattice strain, and decreased transport capability and conductivity in CTLs.

Additionally, the evolution of the PV performance with temperature typically remains unaffected by the intensity of the incident light, as suggested through a comparison of irradiation under AM1.5G (standard solar spectrum on Earth's surface, approximately 1000 W/m^2) and AM0 (solar radiation in outer space, approximately 1360 W/m^2) conditions[32], or by the voltage. These results suggest that, although additional stress factors like light and voltage influence the stability of PSCs, operational stability under thermal cycling are mainly determined by the accumulation of irreversible changes in the materials and devices due to temperature fluctuations. However, testing the cells under a comprehensive combination of all the possible factors should be done to obtain global insights.

Because perovskite materials with different bandgaps exhibit varying properties, such as thermal stability, ion migration behaviour, and mechanical stress tolerance, different responses under temperature cycling are seen. Narrow-bandgap perovskites (<1.5 eV), such as Sn-based or mixed Sn-Pb perovskites, are considered ideal candidates for the bottom cell in tandem solar cells. However, these Sn-containing perovskites are primarily sensitive to oxidation, either from oxygen invasion, acidic poly(3,4-ethylenedioxythiophene)-poly(styrenesulfonate) (PEDOT:PSS)-induced I_2 generation, or residual DMSO sourced from the solution processing of the films. Moreover, these processes can be accelerated under thermal conditions, leading to device failure. Medium-bandgap perovskite materials (1.5–1.7 eV), which are primarily iodide-based (such as $FAPbI_3$) or low-bromide iodide perovskites (when Br < 20%), demonstrate relatively good thermal stability and are promising candidates for use in extreme environments with large temperature fluctuations. Although phase transitions and phase separation can still occur during temperature cycling, additive engineering or interfacial modifications can considerably improve their thermal tolerance. For instance, perovskite cells modified with β-poly(1,1-difluoroethylene) exhibited no signs of fatigue after 1000 hours of maximum power point (MPP) tracking at 75 °C and thermal cycling between −60 and +80 °C[57]. Wide-bandgap perovskite materials (>1.7 eV), such as $FAPbBr_3$, exhibit a high population of defect states and numerous ion migration events. However, the accumulation of ions in wide-bandgap perovskite materials can result in phase separation, bandgap shifts and localized electric field failure under temperature cycling, particularly at elevated temperatures[87]. Enhancing thermal expansion compatibility and developing self-healing perovskite materials offer promising approaches to mitigate the degradation of wide-bandgap perovskites under cyclic temperature changes[88].

[H1] Strategies to enhance the resilience of perovskite photovoltaics to temperature cycling

There are several strategies to enhance the stability of PSCs under temperature cycling. These strategies encompass stabilizing the phase structure of the perovskites, either by improving the crystallinity of the material or by reducing the lattice strain through the incorporation of buffer materials at grain boundaries, mitigating defects using different modifications approaches, and preventing delamination occurring within the perovskite layer itself and at its interfaces with the CTLs by adopting thermally stable and highly conductive CTLs[55,57,89-100].

[H2] Stabilizing the perovskite structure

To ensure the stability of PSCs under temperature cycling, the crystalline stability of the perovskite material must be maintained across the specified temperature range, thereby minimizing potential irreversible effects of phase transitions during temperature fluctuations. As a strategy, molecules such as β-poly(1,1-difluoroethylene) with an ordered dipole structure can regulate the crystallization and energy level alignment of the perovskite film. By acting as molecular springs that dynamically respond to stress within the perovskite lattice, these ordered molecules accommodate lattice strain caused by thermal expansion or contraction, effectively stabilizing the crystal structure and mitigating phase transitions. The device showed no signs of fatigue during rapid thermal cycling between −60 and +80 °C, highlighting the considerable impact of the ordered dipole structure on the operational stability of perovskite solar cells[57]. This is largely linked to the intrinsic stability of the perovskite materials. For example, in the cases of $FAPbI_3$ and $CsPbI_3$, both the large FA^+ ions and the small Cs^+ ions induce distortions in the $[PbX_6]^{4-}$ octahedra, resulting in deviations from the symmetric α-phase (**Fig. 4a**). This structural distortion is a critical factor underpinning their phase instability[101-104]. By adjusting defect formation energy, lattice strain in perovskites can be controlled (**Fig. 4b**). Typically, increasing compression stabilizes defects, while increasing tension destabilizes them. Consequently, the inactive δ-phases possess the lowest free energy of formation at room temperature, rendering it challenging to achieve stability under constant ambient conditions[105,106]. The ionic structure of the perovskite film is also critical in influencing device thermal stability, which is observed through phase transitions in four standard perovskite materials in devices under thermal cycling between −30 and 85 °C[101]. Therefore, for materials like $MAPbI_3$ and mixed-cation perovskites, characterized by a certain degree of stability at ambient conditions, meticulous attention must be paid to the dependency of potential phase transitions to compositional variations. Notably, it was shown that adjustments in the composition can lower the temperature required for phase transition between the cubic phase to inactive phases at low temperatures, extending their applicability in cold environments[63,64].

[H2] Reducing lattice strain

The periphery of the substrate is subject to shear stress, a mechanical effect that is exacerbated by variations in temperature. This phenomenon induces potentially greater displacements within the perovskite lattice, consequently giving rise to lattice distortion or the formation of defects, thereby impacting crucial material properties such as light absorption, charge transport, and carrier lifetime (**Fig. 4c**). Moreover, it can promote alterations in the interface structure between the perovskite and the substrate, thereby influencing interface charge transport dynamics and the sustained efficiency of devices, particularly as they scale up in size[107]. The challenge in addressing this issue predominantly revolves around effectively mitigating and dispersing substrate-induced stress while concurrently preserving the uniformity and structural integrity of the perovskite films (**Fig. 4d**). There is evidence to suggest that rationally applied pressure can instigate phase transitions. Specifically, $MAPbI_3$ can undergo phase transformation at pressures ranging from 0.3 to 0.4 GPa. The residual stress generated during device fabrication can attain 77.7±5.4 MPa under constant room temperature conditions[100]. It was shown that an increase in environmental temperature from −40 to 85 °C led to a discernible reduction in residual strain on the surface of the perovskite[100]. Although this strain may not attain the pressure threshold for phase transition, its evolution during thermal cycling can potentially impact phase transition dynamics. This observation further explains why identical perovskite types exhibit varying phase transition temperatures. Incorporating additives into either the perovskite precursor solution or the antisolvent, which is used as quenching agent during wet device fabrication, facilitates the formation of a dense film typified by larger grains owing to the resulting strain in the modified structure. Such dense perovskite films have improved thermal cycle stability. Phase transition is also related to the compactness of the perovskite; in general, the more compact the film is, the more stable it is[57,108-110]. Using a polymer as a buffer against thermomechanical stress at the crystal interfaces (**Fig. 4e**) improves the perovskite phase stability, suppresses ion diffusion, and produces minimal and recoverable tension changes. Using this strategy, fabricated devices could maintain stable PV performance during sustained thermal cycling between −60 and +80 °C for 120 cycles [57].

[H2] Improving the adhesion at the interfaces

Beyond improving the bulk properties of the perovskite material, it is imperative to regulate detrimental strain and improve the adhesion at the interface between the CTL and the perovskite material to enhance overall device performance. In this sense, an optimal alignment between the CTEs of the perovskite layer and the adjacent layers can prevent fracture and premature delamination. However, maintaining such alignment is challenging, especially considering the simultaneous CTL selection requirements for efficiency optimization. Incorporating molecular interlayers to the stack of the solar cell devices, based on flexible interconnecting molecules to bridge the perovskite and the substrate, could alleviate delamination, thereby contributing to stabilise interfaces and suppress

mechanical failure[49,83-86] (**Fig. 4f**). For instance, introducing a more pliable interlayer beneath the perovskite layer, such as poly(methyl methacrylate) (PMMA), dimethylphenethylsulfonium iodide (DMPESI), or ferrocenyl-bis-thiophene-2-carboxylate (FcTc2), between the perovskite and the CTL, augment stability under thermal cycling conditions (**Fig. 4g**)[21,55,111]. Specifically, PMMA as an interfacial modification layer preserves the crystallinity of the device, which shows almost no efficiency loss after 26 thermal cycles between 290 and 90 K[93]. These interlayers, interfacing with both the perovskite and CTLs, can modulate phase transition points by passivating defects and alleviating interfacial stress. They can also increase adhesion[84,112], consequently increasing the activation energy of ion migration. Additionally, the fracture energy can serve as a key figure of merit for assessing the interfacial mechanical strength of perovskite devices[80-82]. We believe fracture energy could also be increased through mechanical reinforcement techniques or by integrating materials with higher fracture toughness at the interfaces.

Enhancing the elasticity and mechanical stability of perovskites is another requirement for achieving PSCs that are stable during thermal cycling. The introduction of interface buffer layers could stabilise crystal-to-crystal and layer-to-layer contacts, leading to improved mechanical stability. A mechanistic analysis of how the buffer layer alleviates interfacial stress in the perovskite and enhances the device's thermal stability could offer valuable insights for developing new buffer layers. As an example, a protonated aminosilane coupling agent, $(OC_2H_5)_3$-Si-$(CH_2)_3$-NH_3Br (PASCA-Br), was used as a buffer layer between TiO_2 and a perovskite layer. The Si end anchors to the TiO_2 layer, improving adhesion, whereas the R-NH_3Br end serves as a structural component to compensate for defects in the perovskite's octahedral units, reducing lattice distortion. The R-NH_3Br extends the growth sites, helping to release lattice stress, reducing in-plane strain from 1.55 to 0.67% and out-of-plane stress from 1.47 to 0.73%. This strategy effectively mitigates interfacial strain, leading to devices exhibiting higher carrier mobility in a broad temperature range from 223 to 263 K[113]. In addition, incorporating dense, less penetrable buffer layers and/or less reactive electrodes, such as carbon, or electrodes composed of material(s) capable of forming a diffusion barrier, such as chromium,[114,115]would be beneficial to mitigate material interdiffusion.

[H1] Photovoltaic performance under real-world diurnal cycles

Evaluating operational stability under field conditions is crucial before projecting perovskite PVs to market, as[53] real-world results may differ from laboratory tests[116,117]. This discrepancy was shown, for example, after a one-year outdoor test in Berlin, where the outdoor operational stability of the device was longer than when measured in conventional laboratory settings[118]. This result may be attributed to the fact that switching between day and night enables devices to sufficiently recover during the night via strain relaxation. The change of seasons also has a strong impact on the stability of PSCs; the PCE

drops from July to October and November to February, when the weather shows greater temperature changes. PV performance is affected by the exposure to specific absolute temperature values (**Fig. 3**), but prolonged times and consecutive cycles at high temperatures are much more detrimental for long-term device stability (**Fig. 5a**). Meanwhile, other real-world stresses impact device stability[53]. Through a combination of computational simulations and data obtained from outdoor stability tests, it was shown that high temperatures and light strongly determine the stability of PSCs under outdoor conditions **(Fig. 5b-c**). This dependency can be caused by the photon-catalysed degradation of the perovskite materials and the specific combination and compatibility of the layered materials involved in the devices. In this case, the behaviour observed in lab tests correlated with field conditions, enabling the use of indoor accelerated stability tests to predict device performance under outdoor ageing. For the particular case of p-i-n (inverted) PSCs, to improve their stability under temperature cycles and outdoor conditions, a mixture of self-assembled monolayers can be used to improve the interface. Considering the influence of other environmental factors (such as humidity or even rainwater), advancing encapsulation is critical[117]. The PCE of perovskite/silicon tandem solar cells is also strongly dependent to the cyclic nature of operational conditions[119-121].

In particular, we would like to highlight the dependency of the bandgap of the absorber layers to temperature, which leads to variability in the optimal values needed for current matching[119]. Firstly, in perovskite-silicon tandem cells, the optimal bandgap for current matching is about 1.73 eV. However, the c-Si bandgap undergoes a redshift as the temperature increases, whereas the perovskite bandgap undergoes a blueshift, thereby leading to energy level misalignment in the device and impacting its efficiency[119]. Secondly, when tandem cells are deployed in real-world environments, temperature fluctuations or external forces may induce stress within the thin films. Experimental results have shown that when the temperature drops from 80 to 30 °C, the tensile stress in the perovskite film can reach as high as 87.66 MPa[122]. If the compressive stress exceeds the critical stress, it can lead to layer failure or delamination[123]. Moreover, bottom texturization, such as the use of random pyramid textures, substantially improves light coupling efficiency[124]. The top and bottom cells can be interconnected via a nanocrystalline silicon composite junction, combining low tunnel resistance and high shunt elasticity to fully couple near-infrared light into the c-Si bottom cell[125], thereby addressing the effect of temperature-induced bandgap changes in perovskite and c-Si on tandem cell performance. Using a textured silicon surface structure with no sharp angles can additionally alleviate stress concentration issues during thermal cycling[126]. Furthermore, adjusting the size of A-site ions in the perovskite, such as modifying the content of MA^+, Rb^+, or Cs^+ at the A-site, can help relieve the stress between the perovskite and silicon layers[127,128].

[H1] Stability assessment protocols

To better quantify the temperature sensitivity of the performance of PV devices and compare PV parameters variation in different works, the temperature coefficient $\beta_G(T)$ has been introduced[129,130], which characterises the variation of a parameter G (V_{OC}, J_{SC}, FF or PCE) as a function of temperature T:

$$\beta_G(T) = \frac{10^6}{G(T_0)} \frac{G(T) - G(T_0)}{T - T_0} \qquad (1)$$

where T_0 is room temperature (298.15 K) and the temperature coefficients are expressed in ppm K^{-1}.

As reported, in PSCs, the decline of V_{OC} with increasing temperature is lower than that for silicon solar cells[64]. This result, in theory, suggests that PSCs could present higher operational thermal stability, particularly under temperature cycling conditions.

For the long-term stability test of devices, the perovskite PV research community has constructed test standards based on the International Summit on Organic PV Stability (ISOS)[19,56,131]. As the study of stability under temperature cycling is still in its early stage, applying unified test evaluation procedures is particularly important. There are already some protocols related to measuring devices under temperature cycles, such as ISOI-T-1, ISOI-T-2, and ISOI-T-3[19] (**Table 1**). However, these protocols consider too narrow temperature ranges for certain applications and have limited guidelines on other parameters such as the heating and cooling rates. The temperature range suggested by the ISOI-T-3 protocol goes from −45 to +85 °C, which is enough to simulate most terrestrial applications. Meanwhile, for applications in extreme-temperature earth regions and space environments, a broader cycling temperature range is required[36,132]. In space, the absence of atmosphere and resulting lack of heat transfer can cause the surface temperature of aircraft to fluctuate rapidly and considerably between darkness and sunlight. We suggest adopting a wide temperature test range of −160 to 150 °C[32,133]. In addition to specifying the temperature range for the thermal cycle, it is also important to establish guidelines for the temperature ramp rate, cooling rate, and dwell time at specific temperatures. Rapid heating and cooling rates can generate substantial thermal stress within the cell materials. When temperature changes occur too rapidly, the materials may not expand or contract at the comparable rate, leading to increased mechanical stress that can cause microcracks, interfacial failure, or delamination between layers. Therefore, in addition to defining the temperature range for thermal cycling, it is crucial to incorporate heating and cooling rates, as well as specific environmental temperature cycles, into the thermal cycling test protocols. Under light irradiation and at night or when shaded, the simulated accelerated heating or cooling rate could be set more broad to

provide a better approximation of rapid day-night temperature fluctuations, for instance an accelerated thermal cycling protocol with a 20 °C/min ramp rate and a 2-minute thermal equilibrium hold between −60 and +80 °C. This protocol, in addition to established standards (such as ISOS), should expedite the detection of potential failure modes under fluctuating temperatures, providing faster stability insights and identifying weaknesses within devices and encapsulation materials. Notably, this protocol has demonstrated effectiveness in correlating accelerated perovskite degradation with device thermal cycling stability[57]. Additionally, humidity variations can accelerate material degradation, making it essential to include humidity changes in thermal cycling tests. For instance, in inland basin regions, intense solar radiation during the day leads to rapid temperature increases and a drop in humidity, while at night, radiative cooling causes substantial temperature drops and an increase in humidity. This effect is even more pronounced in extremely dry regions, such as deserts, where fluctuations in temperature and humidity are more extreme. Therefore, we recommend incorporating humidity variation as a key parameter in thermal cycling protocols to more accurately simulate real-world conditions and properly evaluate the impact of temperature and humidity on device performance, ensuring comprehensive test results.

As a reference, **Fig. 6** presents a recommended temperature cycle profile ranging from −40 to +85 °C, as outlined in the IEC 61215 standard[134,135]. It suggests a cooling and heating rate during the cycles ranging between 45 and 100 °C h^{-1} (0.75 and 1.67 ºC min^{-1}), with a >10 min dwell time, and a duration for one cycle ideally shorter than 6 h. Also, the applied current during the thermal process can cause local heating in solar cells, potentially inducing high series resistance exceeding the maximum set temperature. The current magnitude depends on the kinds of absorber materials. When characterizing different properties under temperature cycles, it is advisable to indicate the specific temperature and the number of temperature cycles applied. Additionally, the long-term stability may exhibit varying rates when devices are subjected to different temperature intervals[57].

[H1] Outlook

The research on accelerated ageing of PSCs through thermal cycling tests has been relatively limited in comparison to constant temperature measurements. Nevertheless, the more and more evident commercialization potential of perovskite PV is gradually encouraging more studies in this direction. Compared to fixed temperatures, temperature cycles have very different impacts on the evolution of the perovskite materials and PV devices. Hence, efforts to unravel the underlying mechanisms of temperature cycling in PSCs and to develop stable and high-efficient perovskite devices under temperature cycles are still needed. Here, we point out four avenues for improving perovskite stability under temperature cycling and provide suggestions for more realistic temperature cycle stability testing in the future.

The first strategy to improve stability would be to explore more effective and robust ligands with multiple anchoring groups, such as polymers, to serve as strain buffers for adjacent layers with different CTEs and inhibit ion migration between the stack layers. While this approach has been investigated in some studies, its impact under temperature cycling conditions has not been extensively explored, leaving room for further validation in practical scenarios. As a valid strategy to increase the thermal stability of PSCs, achieving robust energy level alignment under temperature cycling is also important to maintain photoconversion efficiency, complementing the function of strain-buffering ligands.

Secondly, selecting an appropriate CTL is crucial to achieving device stability during temperature cycling. Higher temperatures in the cycling process can have a greater impact on certain organic transport layers, such as Spiro-OMeTAD, which may degrade up to +70 °C[136]. Besides considering the thermal stability of the CTL itself, its CTE should also be considered. It is recommended to choose one that closely matches that of the perovskite layer to prevent delamination between the two layers during temperature cycles. Alternatives such as PTAA offer better thermal stability and closer CTE matching with perovskites, while inorganic options like NiO_x provide exceptional thermal stability and interface robustness. Additionally, self-assembled monolayers (SAMs), as one of the leading HTL candidates, present challenges under elevated temperatures where bond breakage may occur. Enhancing their binding properties under thermal stress will be a significant milestone in promoting stable perovskite photovoltaics. Evaluating CTLs based on their thermal behaviour, CTE compatibility, and durability under cycling conditions is essential for improving the performance and longevity of perovskite solar cells.

Thirdly, encapsulation greatly affects the lifetime of devices as it suppresses the internal degradation of volatile species and the external ingression of moisture and reactive species. Significant progress has been made in this area, with materials such as ethylene vinyl acetate (EVA), polyvinyl butyral (PVB) and polyisobutylene (PIB) demonstrating the ability to create effective moisture barriers. These materials are selected based on characteristics, such as their elastic modulus, morphology, and CTE mismatches with the perovskite[49,137,138]. However, challenges remain in achieving encapsulation materials that combine long-term stability with resistance to dynamic thermal cycling. For instance, a rigid encapsulation material possibly departs the perovskite solar cells during thermal cycling, leading to mechanical failure. Future research should focus on developing encapsulation systems with enhanced barrier properties, self-healing mechanisms, and better compatibility with perovskite layers to address delamination and moisture ingress over time. Therefore, suitable encapsulant materials and techniques are highly needed to enhance thermal cycling stability and improve the commercial viability of the device by reducing the impact of temperature cycling on the entire device. It is also important to conduct thermal cycling tests on fully encapsulated devices to evaluate and study the mechanisms involved during temperature cycling.

Finally, the decomposition mechanism of PSCs under temperature cycles remains underexplored. To gain a better understanding of its behaviour, it is crucial to employ consistent and uniform testing methods. Given that high temperatures typically coincide with sunlight exposure, and low temperatures normally occur at nighttime, it would be more meaningful to study the working performance of PSCs at high temperatures and their storage and/or self-healing performance at low or room temperatures within thermal cycling. This approach would provide valuable insights into the behaviour and stability of PSCs under realistic operating conditions. By examining the performance of solar devices during both high-temperature operation and low-temperature storage and/or recovery, researchers can gather comprehensive knowledge relevant to practical applications and address the challenges associated with temperature cycling more effectively. Additionally, to better study the working mechanism of PSCs in real life, other stress combination, such as temperature with humidity, bias or atmosphere should be considered in future research.

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Author contributions

L.W., S.H., F.Y. and G.L. contributed to the writing and editing of this manuscript. L.W. and G.L. prepared the first draft. G.L. and M.L. contributed to the discussion of content and writing. A.A. contributed to the discussion and review of the manuscript. G.L., J.P., M.L. and A.A. supervised the

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Table 1 | Established ISOS protocols that account for the temperature cycling of perovskite materials and devices[19].

Test ID	Light Source	Temperature	Rel. humidity	Environment/Set-up	Load
ISOS-T-1	None	RT to +65, +85 °C	Ambient	Hot plate/Oven	OC

ISOS-T-2		RT to +65, +85 °C	Ambient	Oven/env. chamber	OC
ISOS-T-3		–40 to +85 °C	< 55%	Env. chamber	OC
ISOS-LT-1	Solar simulator	Linear or step ramping between room temp and +65 °C	Monitored, uncontrolled	Weather chamber	MPP or OC
ISOS-LT-2		Linear ramping between +5 to +65 °C	Monitored, controlled at 50% beyond +40 °C	Env. chamber with a sun simulator	MPP or OC
ISOS-LT-3		Linear ramping between −25 to +65 °C	Monitored, controlled at 50% beyond +40 °C	Env. chamber with sun simulator and freezing	MPP or OC

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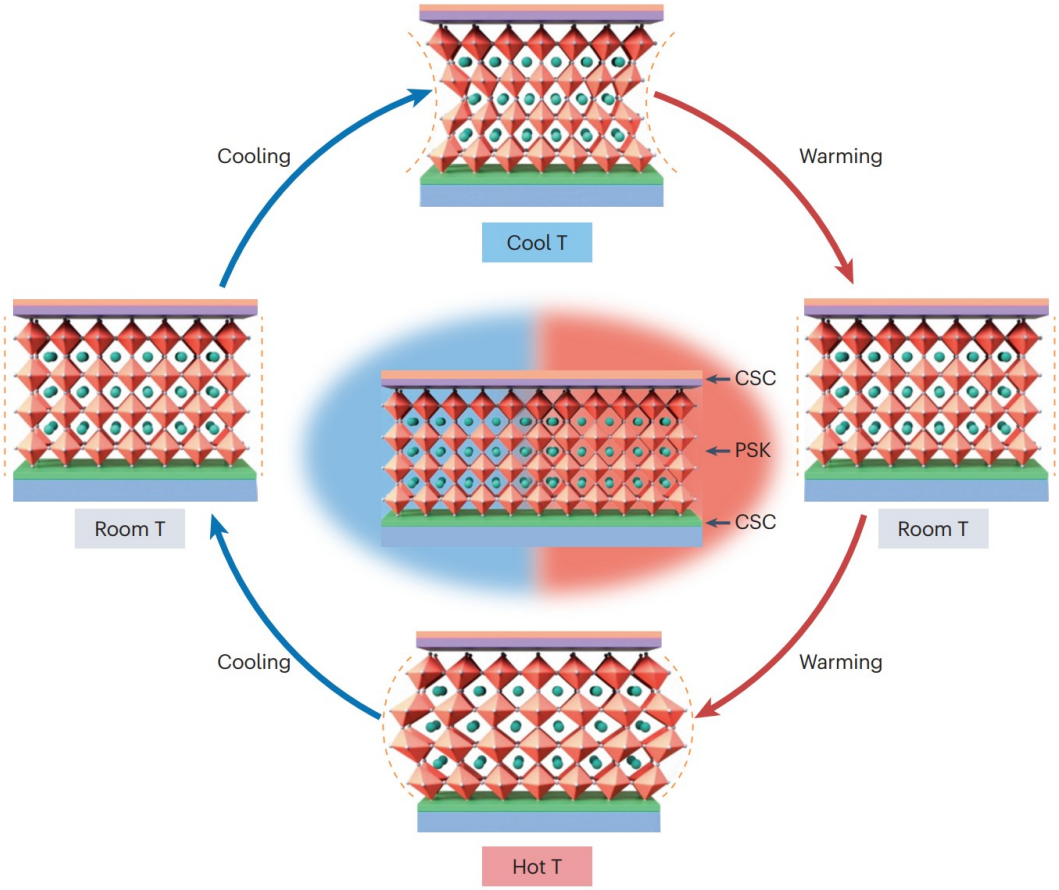

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860 **Fig. 1 |The cycling temperature influence on perovskite structure in the device.** CSC denotes
861 charge selective contact, PSK denotes perovskite, and T denotes temperature.

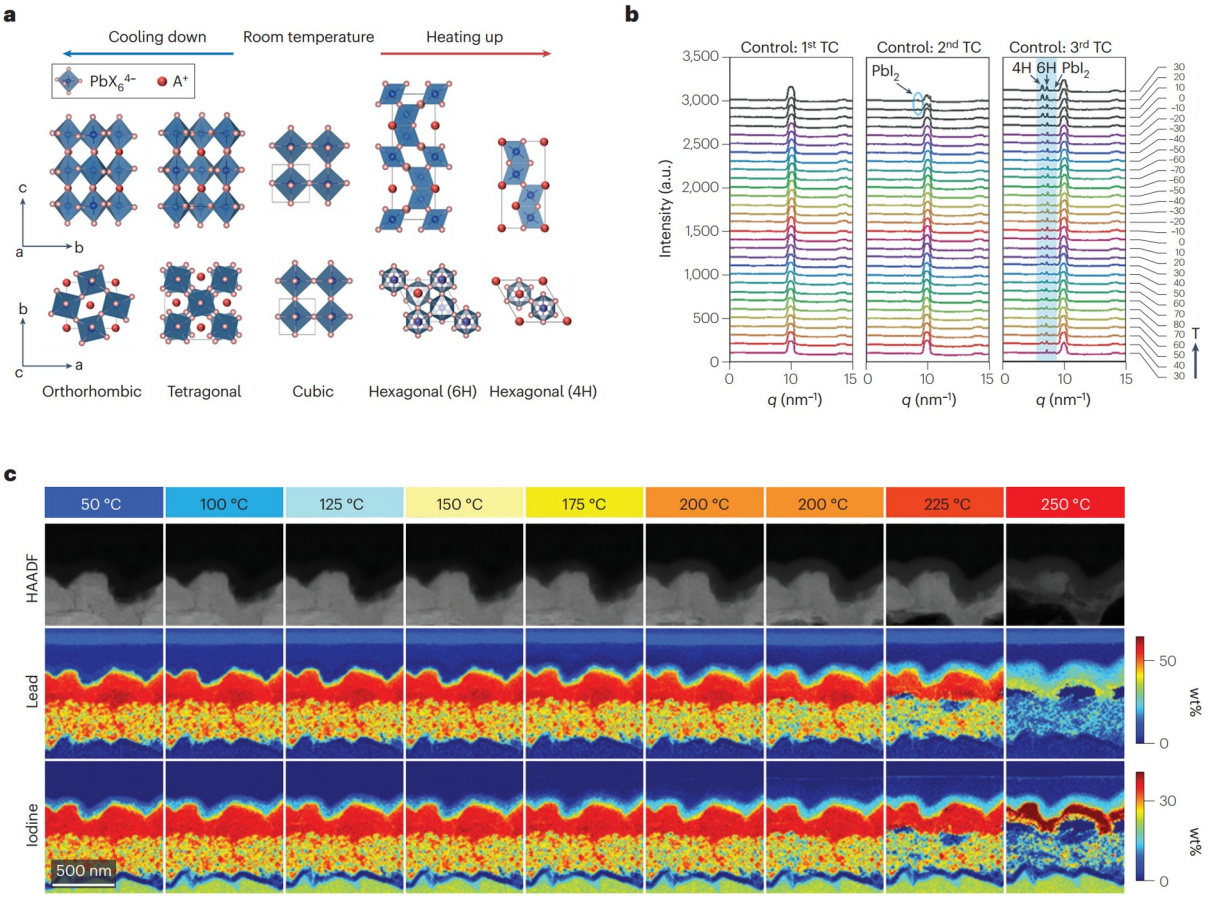
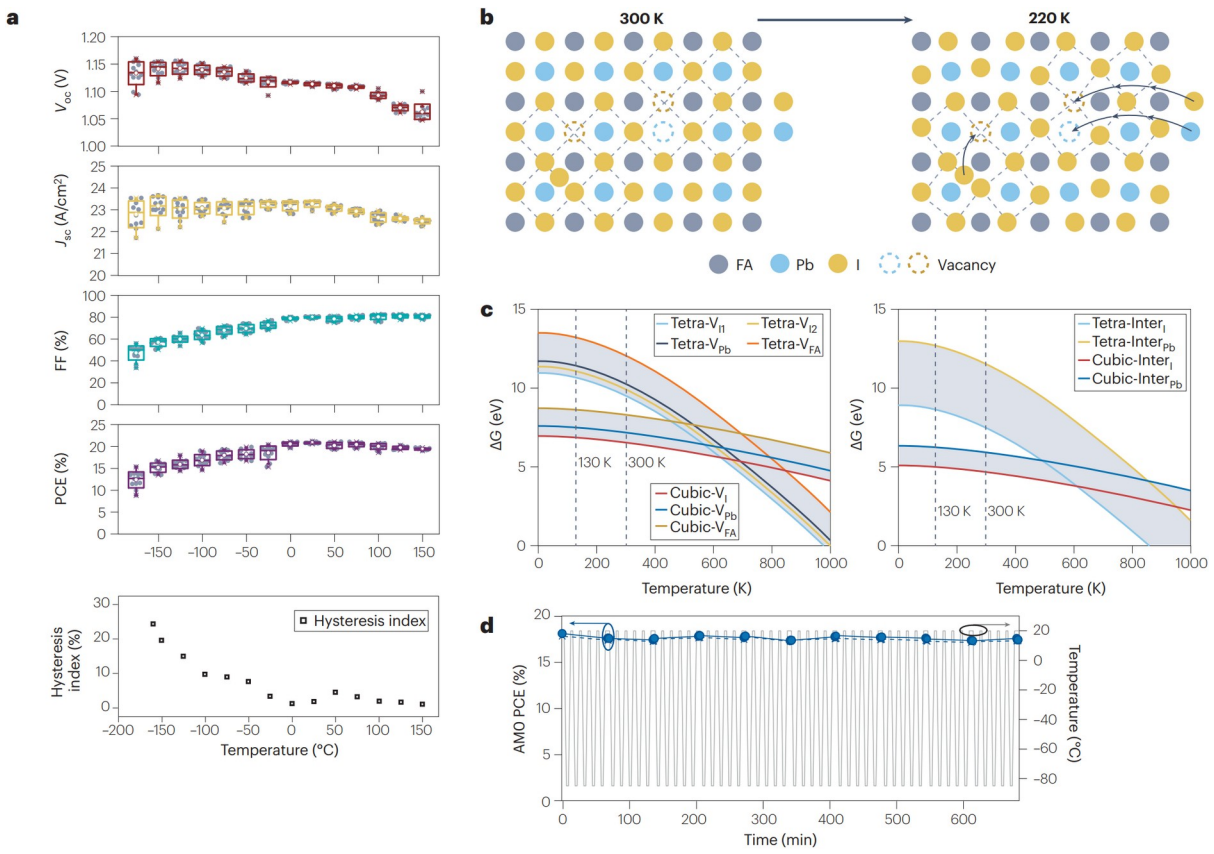


Fig. 2 | Temperature influence on perovskite structure. a, Crystal phases of FAMACs-based perovskites and evolution with temperature (perovskite structure evolution). **b,** Temperature-resolved grazing-incidence wide-angle X-ray scattering (GIWAXS) profiles for FAMACs-based perovskite film with highlighted PbI_2 peak arising (perovskite degradation and phase transition). **c,** High-angle annular dark-field (HAADF) images and energy-dispersive X-ray (EDX) elemental maps for iodine and lead for lamellae perovskite sample obtained after heating at different temperatures (interlayer material degradation). Figure **a** adapted with Permission from refs [139,140]. Figure **b** reprinted with Permission from ref.[57], 2023 AAAS. Figure **c** reprinted with Permission from ref.[71], 2016 Springer Nature Limited.



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873 **Fig. 3 | Device performance parameters with temperature variation. a,** Statistical box charts of
874 V_{OC}, J_{SC}, FF, PCE and HI distribution of PSCs at different temperatures (The temperature-induced
875 changes in PCE are primarily driven by opposing trends in V_{OC}, with PCE following the same trend as
876 FF and HI.). **b,** A schematic illustration for the self-elimination of intrinsic defects via phase transition
877 (Reducing the temperature aids in the elimination of intrinsic point defects in perovskite materials). **c,**
878 The dependence of △G for vacancy and interstitial defects on the temperature. (Lowering the
879 temperature induces a cubic-to-tetragonal phase transition, increasing defect-formation energy.) **d,**
880 Evolution of PCE of PSCs during 50 thermal cycling between +20 and −80 °C at 10 mbar under
881 constant AM0 illumination (blue symbols correspond to the PV parameters on the left, and the solid
882 grey lines refer to the temperature on the right). (Perovskite devices have reversible performance
883 behavior upon returning to room temperature) Figures **a, d** reprinted with Permission from ref.[32], 2022
884 Wiley. Figures **b** and **c** reprinted with Permission from ref.[64], 2020, Elsevier

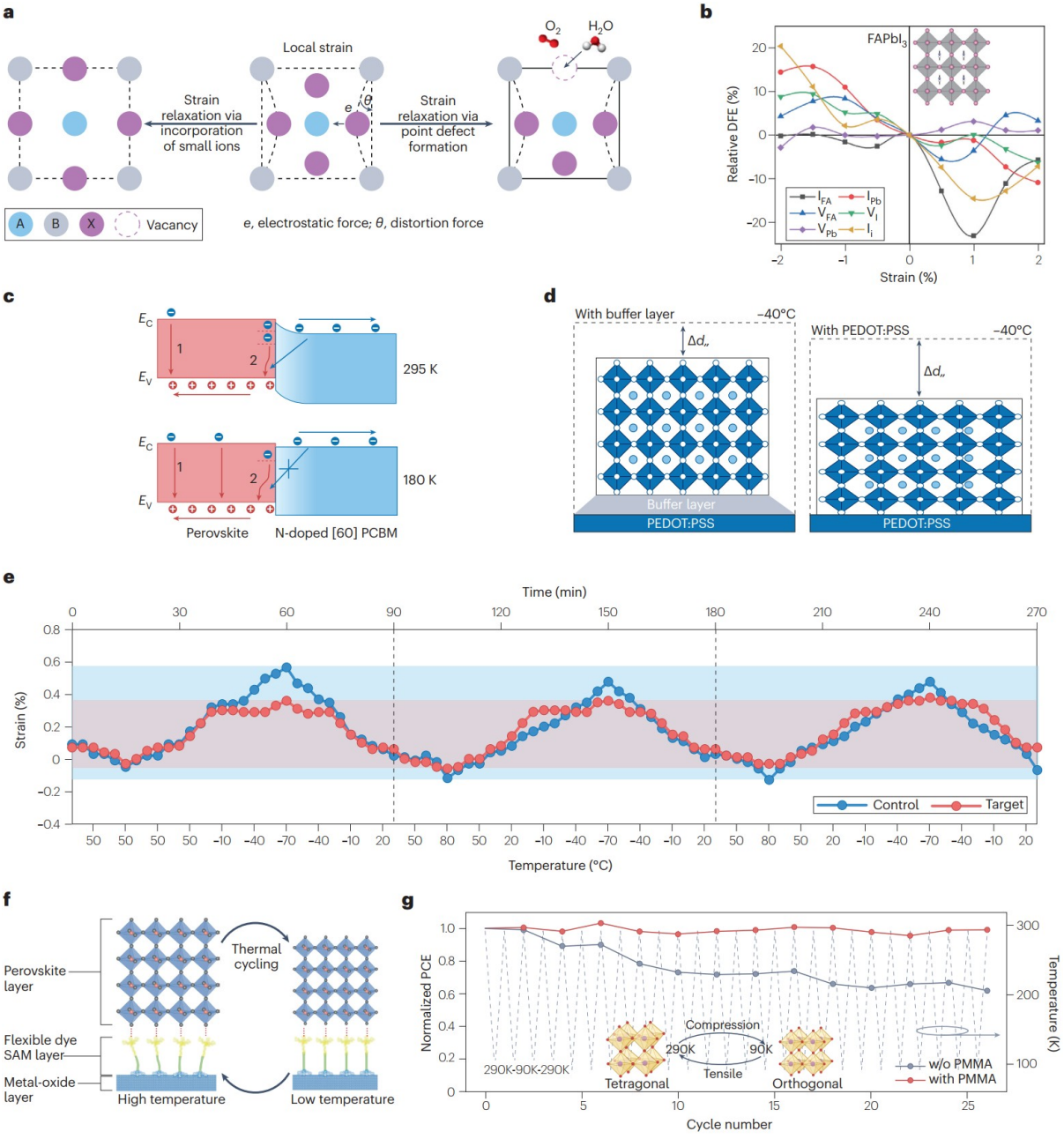

Fig. 4 | Strategies for enhancing photovoltaic performance under thermal cycling. a, Schematic illustrating the local strain, which is decreased by forming point defects or by doping with small ions (Strain relaxation via ion incorporation and defect formation). **b,** Relationship between defect formation energy (DFE) and lattice strain (Defect formation energy control). **c,** Schematic diagram of charge extraction and recombination at the perovskite/N-doped $PC_{61}BM$ interface at maximum power point at 295 (+21.85, top) and 180 K (−93.15 °C, bottom) (Temperature-induced interfacial charge transport). **d,** Schematic diagram of strain formation process and reduction of strain through buried interface modification with or without buffer layer between PEDOT:PSS and Sn perovskites (Buffer layer at the interface). **e,** The temperature-resolved lattice strain for control and target perovskites. (The temperature starts from room temperature, heating to +80 °C and then cooling to −60 °C. The

progress ends at room temperature. The time per complete cycle is 90 min.) (Ordered dipole structure polymer-coated perovskite crystals). **f,** Illustration of how flexible dye self-assembled monolayers (SAMs) alleviate compressive and tensile stresses in perovskite films going through a thermal cycling process (Interfacial flexible SAMs to release strain). **g,** Normalized temperature cycling test between 290 (+16.85) and 90 K (−183.15 °C) for the PSCs with and without PMMA interlayer (Interlayer-regulated thermal cycling performance). Figure **a** reprinted with Permission from ref.[103]. Figure **b** adapted with permission from ref.[104], 2022 Springer. Panel **c** adapted with Permission from ref.[141], CC BY 4.0. Panel **d** reprinted with Permission from ref.[111], 2024 Wiley. Panel **e** reprinted with Permission from ref.[57], 2023 AAAS. Figure **f** reprinted with Permission from ref.[142], 2022 Springer Nature Limited. Figure **g** reprinted with Permission from ref.[93], 2023 Elsevier.

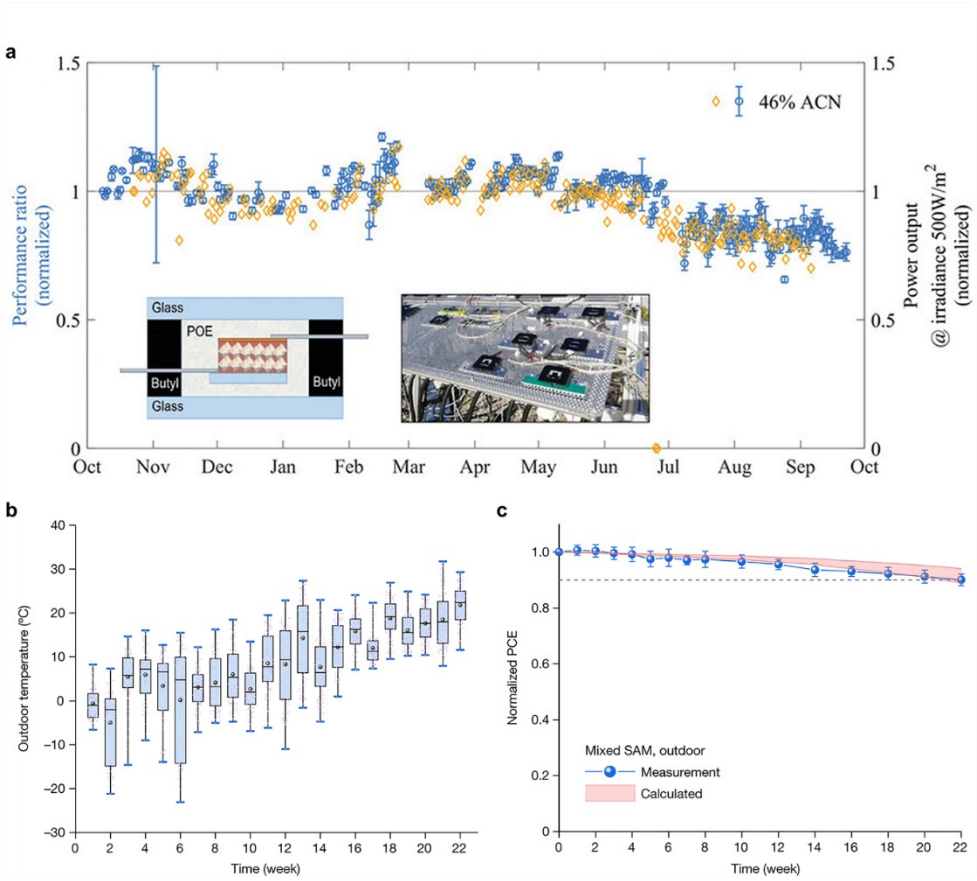

Fig. 5 | PV performance under real-world diurnal cycles. a, Time evolution of normalized performance ratio (averaged over three PSCs) and cell MPP tracking at a power of 500 W m^{-2} during the outdoor exposure (ISOS-O-2) for small area devices. The inserted photographs are the devices fixed outdoors in an open rack configuration and schematics of device encapsulation. **b,** Outdoor ambient temperature trend. **c,** Normalized PCE evolution of packaged devices under outdoor ageing conditions. The pink band shows the calculated trend. The error bars represent the standard deviations

from 14 individual packaged devices. Figure a reprinted with Permission from ref.[118], 2023 Wiley. Figures **b** to **c** reprinted with Permission from ref.[53], 2023 Springer Nature Limited.

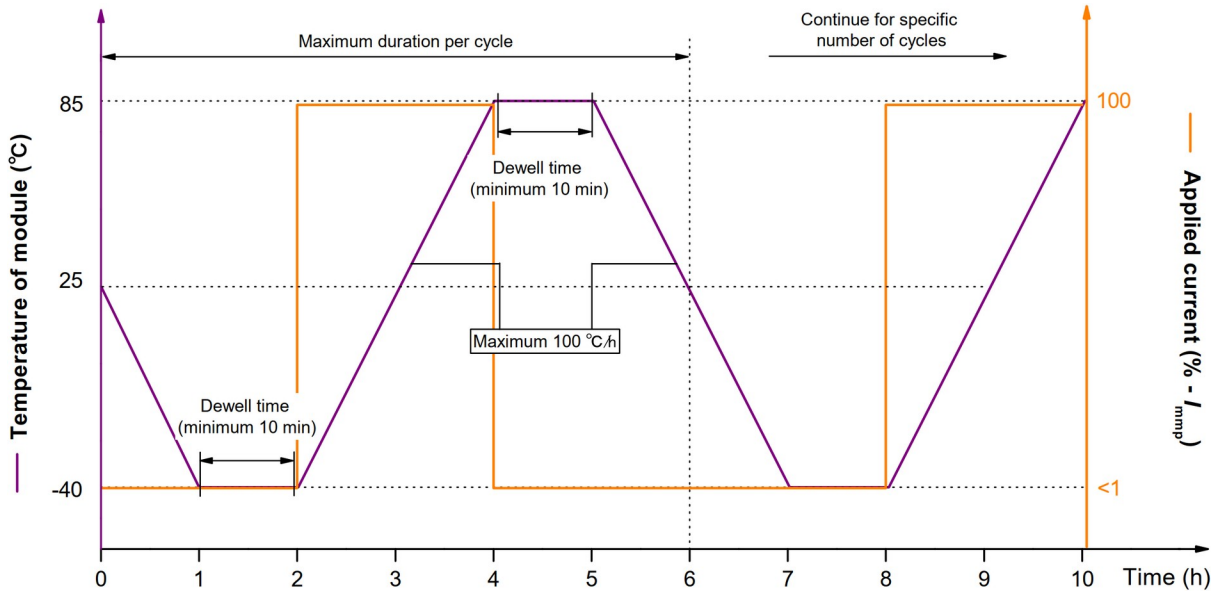

Fig. 6 | Graphic representation of the recommended thermal cycling profile. During the heating from −40 to +80 °C at a current of 10% of the maximum power current for CIGS, CdTe and amorphous silicon. For crystalline silicon, it is 100%. The heating and cooling should not be lower than 45 °C h^{-1}. Sketch of the accelerated ageing tests prescribed by IEC standards.